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APPLICANTS

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** CONTINUING DATA ***** *JMJ*
None

** FOREIGN APPLICATIONS ***** *JMJ*
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Spencer M. Thomas</i> Examiner's Signature Initials	STATE OR COUNTRY ITALY	SHEETS DRAWING 2	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 2
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TITLE
Process for manufacturing a byte selection transistor for a matrix of non volatile memory cells and corresponding structure

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